

SPC2V OSCILLATOR



Specifications

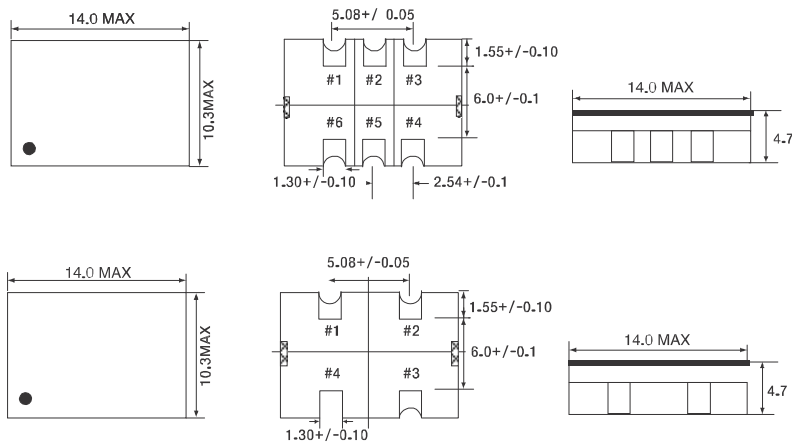
Parameters	Standard	Options	
Frequency range	0,5 MHz - 200,000 MHz		
Logic output	HCMOS/TTL compatible	TTL only	
Frequency stability			
Overall	+/- 50,0 ppm	+/-15,0 ppm best possible	
Initial at 25°C	Please specify		
Temperature			
vs supply voltage change			
vs load change			
Aging first year			
Aging over Years			
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C	
		-40°C ~ +85°C	
		Other on custom request	
Storage temperature	-40°C ~ +85°C	-55°C ~ +125°C	
Supply voltage	5,0 V ± 5% or 3,3 V ±5%	1,8 V ; 2,5 V	
		Other on custom request	
Current	10 - 85 mA max.	Other	
Load	15 pF	Other - dependent of logic output	
Rise/fall time	Other on custom request		
0,5 MHz - 7,999 MHz			8 ns max.
8,000 MHz - 79,999 MHz			6 ns max.
80,000 MHz - 200,000 MHz			3 ns max.
Duty cycle	40/60 %	45/55 %	
		Other on custom request	
Phase noise	Please specify (if required)		
Harmonics			
Sub-harmonics			
Jitter			
Start-up time	10 ms max.	Other on custom request	
Tristate	High or open = enable	no tristate	
	Low = disable		

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Linearity	$\pm 10\%$ max.	Other on custom request
Frequency tuning range	± 50 ppm - ± 300 ppm min	Other on custom request
Pulling slope	Positive	Other on custom request
External control voltage	+ 1,65 VDC $\pm 1,65$ VDC (3,3V) +2,5 VDC $\pm 2,5$ VDC (5,0V)	Other
Marking	Freq + Code + Week +Year	
Package	SP2 4pad	SP2 6pad
		Other SP2 package dimensions available

Drawing



Pin configuration	4 pad version	6 pad version
	#1: Control voltage	#1: Control voltage
	#2: GND	#2: E/D or NC
	#3: Output	#3: GND
	#4: Vcc	#4: Output
		#5: NC
	#6: Vcc	