

SP5L OSCILLATOR



Specifications

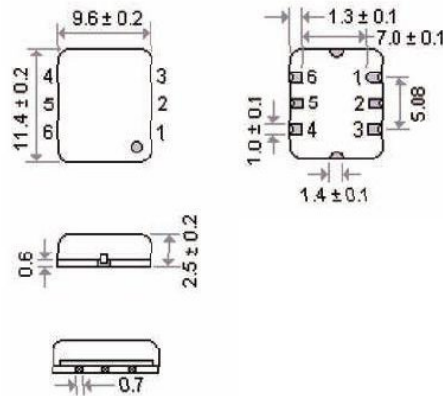
Parameters	Standard	Options
Frequency range	0,750 MHz - 800 MHz	
Logic output	LVDS	
Frequency stability		
Overall	+/- 50,0 ppm	+/-15 ppm best possible
Initial at 25°C	Please specify	
Temperature		
vs supply voltage change		
vs load change		
Aging first year		
Aging over Years		
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C
		-40°C ~ +85°C
		Other on custom request
Storage temperature	-40°C ~ +85°C	-55°C ~ +125°C
Supply voltage	3,3 V +/-5%	
Current	80 mA max	
Load	50 ohm from each output	
Rise/fall time	1 ns max , depends on frequency	
Duty cycle	45/55	
Jitter (12kHz ~20,0 MHz)	4 ps RMS max.	
	0.5 ps max for frequencies 38.0 Mhz - 640 MHz	
Phase noise	Please specify (if required)	
Harmonics		
Sub-harmonics		

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Parameters	Standard	Options
Start-up time	10 ms max.	
Tristate	Low or open = disable High = enable	
Marking	Freq + Code + Week + Year	
Package	SP5 6pad , E/D pad 1	SP5 6pad , E/D pad 2

Drawing



Pin configuration	#1: E/D	#1: NC
	#2: NC	#2: E/D
	#3: GND	#3: GND
	#4: Output	#4: Output
	#5: Complementary output	#5: Complementary output
	#6: Vcc	#6: Vcc