

# SP5CVH OSCILLATOR



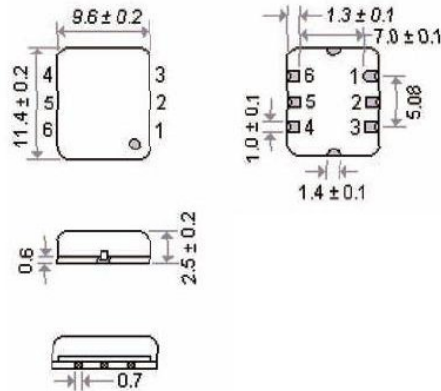
Specifications		
Parameters	Standard	Options
Frequency range	50.0 MHz - 800.0 MHz	
Logic output	CMOS	
Frequency stability		
Overall	+/- 50,0 ppm	+/-15 ppm best possible
Initial at 25°C	included in overall tolerance	
Temperature		
vs supply voltage change		
vs load change		
Aging first year		
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C
		-40°C ~ +85°C
		Other on custom request
Storage temperature	-55°C ~ +125°C	
Supply voltage	3,3 V +/-5%	
Current	40 mA max	
Load	15 pF	
Rise/fall time	2.4 ns typ. , depends on frequency	
Duty cycle	45/55	
Jitter ( 12kHz ~20,0 MHz )	4 ps RMS max. <b>0.5 ps max RMS for frequencies 50.0 Mhz - 320 MHz</b>	
Phase noise	Please specify ( if required )	
Harmonics		
Sub-harmonics		
Linearity	± 10% max.	
Frequency tuning range	± 80 ppm min	Other on custom request
Pulling slope	Positive	
External control voltage	+ 1,65 VDC ±1,65 VDC	

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## Specifications

Parameters	Standard	Options
Start-up time	10 ms max.	
Tristate	Low or open = disable High = enable	
Marking	Freq + Code + Week +Year	
Package	SP5 6 pad	

## Drawing



Pin configuration	#1: V control	
	#2 : E/D	
	#3 : GND	
	#4 : Output	
	#5 : NC / E/D	
	#6 : Vcc	