

DLP OSCILLATOR



Specifications

Parameters	Standard	Options
Frequency range		
+3,3 Vcc	1,000 MHz - 100,000 MHz	
+5,0 Vcc	1,000 MHz - 133,000 MHz	
Logic output	HCMOS	TTL
Frequency stability		
Overall	+ 50,0 ppm	+ -25 ppm best possible
Initial at 25°C	Please specify	
Temperature		
vs supply voltage change		
vs load change		
Aging first year		
Aging over Years		
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C
		-40°C ~ +85°C
Storage temperature	-55°C ~ +125°C	
Supply voltage	5,0 V ± 5%	3,3 V ± 5% (max. 100,000 MHz)
Current		
+ 5 Vcc	45 mA max.	
+ 3,3 Vcc	25 mA max.	
Load	15 pF	
Rise/fall time	3 ns max.	
Duty cycle		
< 66 MHz	45/55 %	
> 66 MHz	40/60 %	
RMS jitter		
< 33,000 MHz , 5 V	± 40 ps max.	
> 33,000 MHz , 3,3 V	± 30 ps max.	

DLP OSCILLATOR

Specifications

Parameters	Standard	Options
Start-up time	10 ms max.	
Tristate	enable/disable	
Marking	Freq + Code + Week + Year	

Drawing

Package	DIL14	
<p>The drawing consists of three views: <ul style="list-style-type: none"> Top View: Shows a rectangular package with a width of 15.24 ± 0.2 mm and a height of 7.62 ± 0.3 mm. There are four pins labeled #1, #7, #14, and #8. The distance between pins #1 and #14 is 4.58 ± 0.5 mm. Side View: Shows the package height H and a lead length of 6.35 ± 0.5 mm. The lead diameter is $\varnothing 0.457 \pm 0.1$ mm and the lead thickness is 0.9 mm MAX. Front View: Shows a width of 20.7 mm MAX and a height of 13.6 mm MAX. A marking point #1.MARK is indicated. </p> <p>$H = 5.08$ mm</p>		
Pin configuration	#1: E/D	
	#7 : GND	
	#8 : Output	
	#14 : Vcc	