

DLLV OSCILLATOR



Specifications

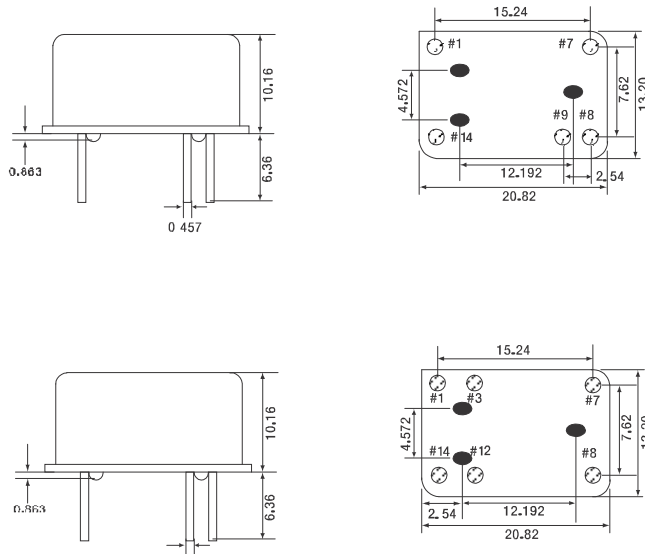
Parameters	Standard	Options
Frequency range	19,440 MHz - 700 MHz	
Logic output	LVDS	
Frequency stability		
Overall	+/- 50 ppm	+/-15 ppm best possible
Initial at 25°C	Please specify	
Temperature		
vs supply voltage change		
vs load change		
Aging first year		
Aging over Years		
Operating temperature	0° C ~ +70°C	-20°C ~ +70°C
		-40°C ~ +85°C
		Other on custom request
Storage temperature	-40°C ~ +85°C	-55°C ~ +125°C
Supply voltage	3,3 V ±5%	
Current	45 mA max	
Load	100 ohms	
Rise/fall time	2 ns max	
Duty cycle	40/60 %	45/55 %
		Other on custom request
Jitter (12kHz ~20,0 MHz)	< 184,320 MHz : 1ps RMS	Other on custom request
	> 184,320 MHz : 4,5 ps RMS	
Phase noise	Please specify (if required)	
Harmonics		
Sub-harmonics		
Start-up time	10 ms max.	Other on custom request
Linearity	± 10% max.	Other on custom request
Frequency tuning range	± 50 ppm - ± 200 ppm min	Other on custom request
Pulling slope	Positive	Other on custom request
External control voltage	+ 1,65 VDC ±1,65 VDC	

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Parameters	Standard	Options
Tristate	no tristate	Low or open = disable High = enable
Marking	Freq + Code + Week + Year	
Package	DIL14 5 pin	DIL14 6 pin

Drawing



Pin configuration	DIL14 5pin	DIL14 6pin
	#1 : Control voltage	#1 : Control voltage
	#7 : GND	#3 : E/D
	#8 : Output	#7 : GND
	#9 : Complementary Output	#8 : Output
	#14 : Vcc	#12 : Complementary Output
		#14 : Vcc